

Amendment of Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-11 (canceled)

12. (currently amended) In a dot matrix display having a plurality of scan lines and a plurality of signal lines arranged in matrix form and a plurality of display elements, one display element coupled between each scan line and signal line at an intersecting point, a circuit to reduce erroneous activation of the display elements comprising;

a reduced voltage source having an output voltage less than a voltage used to drive the display elements;

a discharge circuit for coupling each of the scan lines to the voltage used to drive the display elements and to the reduced voltage source at a time when none of the signal lines is activating the display elements, whereby charge accumulating at the display elements is discharged to reduce erroneous activation of the display elements.

13. (previously presented)The display of Claim 12 wherein the dot matrix display is a light emitting diode (LED) display.

14. (previously presented)The display of Claim 13 wherein the discharge circuit comprises a plurality of LEDs, one of the LEDs being coupled between each scan line and the reduced voltage source for discharging charge accumulating on each of the LEDs and on the scan line.

15. (previously presented)The display of Claim 14 wherein the discharge circuit further comprises a switch coupled between one electrode of each of the LEDs and the reduced voltage source.

16. (previously presented) The display of Claim 15 wherein the plurality of LEDs is arranged in one column of the dot matrix display, the LEDs of the one column being covered so as not to form a visible part of the display.

17. (currently amended) In a dot matrix display having a plurality of scan lines and a plurality of signal lines arranged in matrix form a plurality of display elements, one display element coupled between each scan line and signal line at an intersecting point, a circuit to reduce erroneous activation of the display elements comprising;

a reduced voltage source having an output voltage less than a voltage used to drive the display elements;

a discharge circuit for coupling each of the scan lines to the voltage used to drive the display elements and to the reduced voltage source at a time when one of the signal lines is not activating display elements, whereby charge accumulating at the display elements is discharged to reduce erroneous activation of the display elements;

wherein the discharge circuit comprises a plurality of light emitting diodes (LEDs), one LED being coupled between each scan line and the reduced voltage source for discharging charge accumulating on each of the LEDs and on the scan line, and

wherein the discharge circuit comprises a plurality of switches, one of the switches being coupled between each of the LEDs and the reduced voltage source.

18. (previously presented) The display of Claim 14 wherein the discharge circuit further comprises a current source coupled between each switch and the reduced voltage source.

19. (previously presented) The display of Claim 17 wherein the discharge circuit further comprises a current source coupled between each of the switches and the reduced voltage source.

20. (canceled)

21. (previously presented) The display of Claim 12 wherein the discharge circuit further comprises a plurality of scan line buffer circuits each coupling one of the scan lines to the reduced voltage source when not driving the display element.

22. (previously presented) The display of Claim 21 wherein each of the buffer circuits couples the respective scan line to a driving voltage source when it is driving the display element.

23. (previously presented) The display of Claim 12 wherein the reduced voltage source is at the reference potential.

24. (previously presented) The display of Claim 15 wherein the reduced voltage source is at a reference potential.

25. (previously presented) The display of Claim 17 wherein the reduced voltage source is at a reference potential.

26. (previously presented) The display of Claim 18 wherein the reduced voltage source is at a reference potential.

27. (previously presented) The display of Claim 12 wherein the display elements are discharged between the start of each horizontal scanning period and a time of increase for each drive signal.